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PATENT
5500-68500/TT4418

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/061,671

Filed: February 1, 2002

Inventor:

Michael A. Filippo

Examiner: Wang, Albert C.

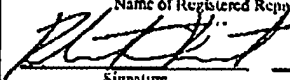
Group/Art Unit: 2115

Atty. Dkt. No: 5500-68500

Title: Apparatus and Method for
Selective Clock Control to
Reduce Power Consumption
in an Integrated Circuit

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July 29, 2005
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SUPPLEMENTAL AMENDMENT

Mail Stop AP
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Please enter the following amendment in the above-captioned case.

Please amend the case as listed below.

10/061,671 (5500-68500/TT4418)

Meyertons, Hood, Kivlin, Kowert & Goretz, P.C.